

**REPLY UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER 2800**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Papadopoulou et al.	Conf. No.:	2038
Serial No.:	10/708,039	Art Unit:	2825
Filing Date:	02/04/2004	Examiner:	Parihar, Suchin
Title:	IC DESIGN MODELING ALLOWING DIMENSION-DEPENDENT RULE CHECKING	Docket No.:	BUR920030149US1 (IBMB-0034)

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AFTER-FINAL AMENDMENT

Sir:

I. INTRODUCTORY COMMENTS:

This paper is being filed in response to the Office Action dated July 30, 2007. Please reconsider the above-referenced patent application in view of the following amendments and remarks: